

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
IMEC32.004C1APPLICATION NO.
09/696,838INFORMATION DISCLOSURE STATEMENT
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
<i>[Signature]</i>	5022047	8/4/91	Dixon et al.			
<i>[Signature]</i>	5235815	8/10/93	Omura			
<i>[Signature]</i>	5309474	5/3/94	Gilhausen et al.			
<i>[Signature]</i>	5357541	10/18/94	Cowart			
<i>[Signature]</i>	5358625	10/25/94	Vander Mey et al.			
<i>[Signature]</i>	5363401	11/8/94	Lucas et al.			
<i>[Signature]</i>	5375140	12/20/94	Bustamante et al.			
<i>[Signature]</i>	5414728	5/9/95	Zehavi			
<i>[Signature]</i>	5742840	4/21/98	Hansen et al.			
<i>[Signature]</i>	5784649	7/21/98	Begur et al.			
<i>[Signature]</i>	5794060	8/11/98	Hansen et al.			
<i>[Signature]</i>	5809321	9/15/98	Hansen et al.			
<i>[Signature]</i>	5822603	10/13/98	Hansen et al.			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

EXAMINER INITIAL	OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)	
<i>[Signature]</i>	1.	Van Himbeek, C., et al., "Silicon Integration of a Flexible CDMA/QPSK Mobile Communication Modem", SYMP. DELFT, 1993.
<i>[Signature]</i>	2.	Philips, L. et al. "Silicon Synthesis of a Flexible CDMA/QPSK Mobile Communication Modem", DSP Applications, pp. 48-58, February 1994.

EXAMINER	DATE CONSIDERED
<i>[Signature]</i>	2-26-04
*EXAMINER: INITIAL IF CITATION CONSIDERED, WHETHER OR NOT CITATION IS IN CONFORMANCE WITH MPEP 609; DRAW LINE THROUGH CITATION IF NOT IN CONFORMANCE AND NOT CONSIDERED, INCLUDE COPY OF THIS FORM WITH NEXT COMMUNICATION TO APPLICANT.	

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Technology Center 2100EXAMINER
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OTHER DOCUMENTS (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.)

3. Van Himbeek, C., "Technological Evolution of CDMA Modems for Mobile Satellite Communication", 4th Int. Workshop on DSP Techniques for Space, pp. 6.27-6.31, September 24-28, 1994, London.
4. Product Brief from Stanford Telecom®, "Digital, Fast Acquisition Spread Spectrum Burst Processor STEL-2000A, 1994.
5. S20043 Data Sheet from American Microsystems, Inc., Spread Spectrum Transceiver, January 1985, pp. 5-10,
6. AT48802, Spread-Spectrum Signal Processor Integrated Circuit, Advanced Information.
7. Zilog Wireless Datacommunications Development Kit information sheet, 1994.
8. Berrebi, E., et al., "Combined Control Flow Dominated and Data Flow Dominated High-Level Synthesis", Proceedings of the 33rd Design Automation Conference 1996, Las Vegas, June 3-7, 1996. pp. 573-578.
9. Ismail, Tarek et al., "Synthesis Steps and Design Models for Codesign", Computer, 28 February 1995, No. 2, pp. 44-52.
10. Valderrama, C.A. et al., "A Unified Model for Co-Simulation and Co-Synthesis of Mixed Hardware/Software Systems", IEEE, 06 March 1995, pp. 180-184.
11. Woo, Nam S. et al., "Codesign from Cospecification", Computer, 27 January 1994, No. 1, pp. 42-47.
12. Paulin, Pierre et al, "High-Level Synthesis and Codesign Methods: An Application to a Videophone Codec", IEEE, 18 Sept. 1995, pp. 444-451.
13. Vahid, Frank, "SpecCharts: A VHDL Front-End for Embedded Systems", IEEE, June 1995, No. 6, pp. 694-706.
14. Narayan Sanuiv, et al., "System Specification with the SpecCharts Language", IEEE Design & Test of Computers, December 1992, pp. 6-13.
15. Buck, Joseph, et al. "Ptolemy: A Framework for Simulating and Prototyping Heterogeneous Systems", International Journal in Computer Simulation 4, 1994, pp. 155-182.
16. Berrebi, E., et al., "Combined Control Flow Dominated and Data Flow Dominated High-Level Synthesis", Proceedings of the 33rd Design Automation Conference 1996, Las Vegas, June 3-7, 1996. pp. 573-578.
17. Ismail, Tarek et al., "Synthesis Steps and Design Models for Codesign", Computer, 28 February 1995, No. 2, pp. 44-52.
18. Valderrama, C.A. et al., "A Unified Model for Co-Simulation and Co-Synthesis of Mixed Hardware/Software Systems", IEEE, 06 March 1995, pp. 180-184.
19. Woo, Nam S. et al., "Codesign from Cospecification", Computer, 27 January 1994, No. 1, pp. 42-47.
20. Paulin, Pierre et al, "High-Level Synthesis and Codesign Methods: An Application to a Videophone Codec", IEEE, 18 Sept. 1995, pp. 444-451.
21. Vahid, Frank, "SpecCharts: A VHDL Front-End for Embedded Systems", IEEE, June 1995, No. 6, pp. 694-706.
22. Narayan Sanuiv, et al., "System Specification with the SpecCharts Language", IEEE Design & Test of Computers, December 1992, pp. 6-13.
23. Buck, Joseph et al. "Ptolemy: A Framework for Simulating and Prototyping Heterogeneous Systems", International Journal in Computer Simulation 4, 1994, pp. 155-182.

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